

AMENDMENTS TO THE CLAIMS

Claim 1 (Currently Amended) A display apparatus, comprising:
a plurality of display panels, each showing different displays;
~~a single display panel driver shared by the plurality of display panels;~~
a single display panel driving unit for ~~controlling the display panel driver to thereby~~
commonly ~~operate~~ operating the plurality of display panels; and
a connection switch for physically and electrically inter-connecting the single display
panel driving unit with the display panels,
wherein the display panel driving unit includes:
a display panel driver shared by the display panels; and
a display path control unit for controlling the display panel driver,
wherein a different portion of the display panel driver is enabled by the ~~single display~~
path control unit ~~panel driving unit~~ according to which one of the plurality of display panels are
activated.

Claim 2 (Original) The display apparatus as recited in claim 1, wherein the display
panels includes a first display panel and a second display panel, and rear sides of the first and the
second display panels face each other with the display panel driving unit disposed between the
first and the second display panels.

Claim 3 (Previously Presented) The display apparatus as recited in claim 1, wherein
the connection switch is formed by using one method selected from a group consisting of a tape
carrier package (TCP) method, a chip on film (COF) method, a chip on board (COB) method and
a chip on glass (COG).

Claims 4-5 (Canceled)

Claim 6 (Previously Presented) The display apparatus as recited in claim 1, wherein
the display panel driving unit further comprises:

a CPU interface control unit for controlling constitution elements included in the display panel driving unit by receiving a command from an external host or a central processing unit;

a display panel control unit for controlling the display panel with an external control signal transmitted through the CPU interface control unit or an independent port;

a memory unit for storing data displayed on the display panels;

X and Y address decoders for selecting a corresponding address of the memory unit by decoding an encoding signal outputted from the display panel control unit;

a register unit for informing each independent operation condition of the display panels;

a timing control unit for controlling a point of time for decoding, latching and displaying a data for the corresponding display panel by the information obtained from the register unit;

a line address decoder for decoding an address for the data of the corresponding display panel at a line unit by responding to an output of the timing control unit;

a latch unit for latching the data corresponding to the address decoded at the line unit, wherein the data is transferred from the memory unit; and

a voltage generation unit for supplying a power voltage for operating each display panel.

Claim 7 (Original) The display apparatus as recited in claim 6, wherein the display panels share the X and the Y address decoders, the line address decoder, the voltage generation unit, the memory unit and register unit during a concurrent and cooperative operation.

Claim 8 (Original) The display apparatus as recited in claim 6, wherein the voltage generation unit comprises:

a voltage converter and a DC/DC booster controlled by on-off states of the first and second display panels.

Claim 9 (Canceled)

Claim 10 (Previously Presented) The display apparatus as recited in claim 1, wherein the display panel driving unit is packaged in a flexible “U” or “S” shape.

Claim 11 (Canceled)